

**SHANTILAL SHAH ENGINEERING COLLEGE BHAVNAGAR**  
**INSTRUMENTATION & CONTROL ENGINEERING DEPARTMENT**

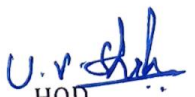
**NOTICE**

**Remedial/Backlog Exam & Internal Submission Schedule (4<sup>th</sup> Semester)**

Backlog Exam Time: - 01:00 PM  
Backlog Exam Venue : - 225 (IC Department)  
Time Duration of Backlog Exam :- 1 hour 30 Minutes  
Backlog Exam Marks :- 30

Internal Submission : - 11:00 AM to 11:30 AM

Date (Day)	Subject Code	Subject Name
05/05/2025 (Monday)	3141706	Analog Signal Processing
06/05/2025 (Tuesday)	3141007	Professional Ethics
06/05/2025 (Tuesday)	3140005	Design Engineering 1 B
07/05/2025 (Wednesday)	3141710	Microprocessor and Interfacing
08/05/2025 (Thursday)	3141708	Control Theory
09/05/2025 (Friday)	3141709	Principle of Measurement Science

  
HOD  
(IC Department)