

SHANTILAL SHAH ENGINEERING COLLEGE BHAVNAGAR
INSTRUMENTATION & CONTROL ENGINEERING DEPARTMENT

NOTICE

Backlog Exam/Internal Submission Schedule (4th Semester)

Backlog Exam Time: - 11:00 AM
Backlog Exam Venue : - 217 (IC Department)
Time Duration of Backlog Exam :- 1 hour 30 Minutes
Backlog Exam Marks :- 30

Internal Submission : - 01:00 PM onwards

Date (Day)	Subject Code	Subject Name
11/10/2024 (Friday)	3141708	Control Theory
14/10/2024 (Monday)	3141709	Principle of Measurement Science
16/10/2024 (Wednesday)	3141710	Microprocessor and Interfacing
17/10/2024 (Thursday)	3141706	Analog Signal Processing
18/10/2024 (Friday)	3141007	Professional Ethics
18/10/2024 (Friday)	3140005	Design Engineering 1 B


HOD
(IC Department)