

SHANTILAL SHAH ENGINEERING COLLEGE BHAVNAGAR
INSTRUMENTATION & CONTROL ENGINEERING DEPARTMENT


NOTICE

Remedial/Backlog Exam & Internal Submission Schedule (4th Semester)

Backlog Exam Time : - 11:00 AM
Backlog Exam Venue : - 225 (IC Department)
Time Duration of Backlog Exam :- 1 hour 30 Minutes
Backlog Exam Marks :- 30

Internal Submission : - 01:00 PM to 02:00 PM

Date (Day)	Subject Code	Subject Name
20/05/2024 (Monday)	3141708	Control Theory
21/05/2024 (Tuesday)	3141709	Principle of Measurement Science
22/05/2024 (Wednesday)	3141007	Professional Ethics
22/05/2024 (Wednesday)	3140005	Design Engineering 1 B
23/05/2024 (Thursday)	3141706	Analog Signal Processing
24/05/2024 (Friday)	3141710	Microprocessor and Interfacing


HOD

(IC Department)