

SHANTILAL SHAH ENGINEERING COLLEGE BHAVNAGAR
INSTRUMENTATION & CONTROL ENGINEERING DEPARTMENT

NOTICE

Backlog Exam/Internal Submission Schedule (4th Semester)

Backlog Exam Time: - 11:00 AM

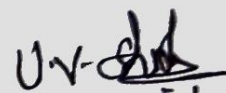
Backlog Exam Venue : - 217 (IC Department)

Time Duration of Backlog Exam :- 1 hour 30 Minutes

Backlog Exam Marks :- 30

Internal Submission : - 01:00 PM onwards

Date (Day)	Subject Code	Subject Name
09/10/2023 (Monday)	3141007	Professional Ethics
09/10/2023 (Monday)	3140005	Design Engineering 1 B
10/10/2023 (Tuesday)	3141708	Control Theory
11/10/2023 (Wednesday)	3141710	Microprocessor and Interfacing
12/10/2023 (Thursday)	3141706	Analog Signal Processing
13/10/2023 (Friday)	3141709	Principle of Measurement Science


HOD

(IC Department)