

SHANTILAL SHAH ENGINEERING COLLEGE BHAVNAGAR

INSTRUMENTATION & CONTROL ENGINEERING DEPARTMENT

NOTICE

Backlog Exam Schedule (4th Semester)

Exam Time: - 11 :00 AM

Exam Venue : - 225 (IC Department)

Time Duration of Exam :- 1 hour 30 Minutes

Marks :- 30

DATE/DAY	Subject
16/11/2022 WEDNESDAY	Principle of Measurement Science (3141709)
17/11/2022 THURSDAY	Analog Signal Processing (3141706)
18/11/2022 FRIDAY	Control Theory (3141708)
19/11/2022 SATURDAY	Microprocessor and Interfacing (3141710)
21/11/2022 MONDAY	Professional Ethics (3141007)

Note :

- Students who do not appear in the examination will be considered as **ABSENT** in the test.


HOD

(IC Department)