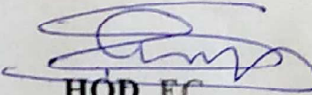


SHANTILAL SHAH ENGINEERING COLLEGE, BHAVNAGAR
DEPARTMENT OF ELECTRONICS & COMMUNICATION

GTU PAT For B.E. SEM 4,6 & 8

TIME TABLE – MARCH 2019

B.E. SEM - 8			
1	25/03/2019	RNA	3:30pm to 4:30pm
B.E. SEM - 6			
1	25/03/2019	DC	11:30am to 12:30pm
2	26/03/2019	AWP	11:30am to 12:30pm
3	27/03/2019	OC	11:30am to 12:30pm
4	28/03/2019	VLSI	11:30am to 12:30pm
5	29/03/2019	TSSN	11:30am to 12:30pm
B.E. SEM - 4			
1	25/03/2019	CSE	1:30pm to 2:30pm
2	26/03/2019	MPI	1:30pm to 2:30pm
3	27/03/2019	ACD	1:30pm to 2:30pm
4	28/03/2019	S&S	1:30pm to 2:30pm
5	29/03/2019	EMI	1:30pm to 2:30pm


HOD EC