**Shantilal Shah Engineering College, Bhavnagar**

**Department of Electronics & Communication**

**Subject: Microcontroller & interfacing**

**Test 1 (Mark: 60)**

 As per Progressive assessment plan, the third and last Open Book test for the subject is arranged on **1/9/18** in room number **D-115** and **D-116** at **11.15 a.m.**

The syllabus for the same is as per the following table.

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **Sr. No.** | **Exam Date** | **Marks** | **Course From the Syllabus** | **Course From the Assignment** | **Course From the Tutorial** |
| 1 | 01/10/2018 | 60 | Topic No.1- 4 | All Questions | All Program |

 Prof. A. J. Mankad Prof. S. B. Parmar

 (Faculty coordinator) Head (EC)